IN THE CLAIMS

Please find the claims to be in the form as follows:

Claim 1 (previously presented): A method for processing video source frames within a display device, comprising:

employing a video processing system (VPS) as part of the display device that includes a processor, a memory structure, and a video input device, wherein the processor is coupled to the memory structure and to the video input device;

inputting video source frames from a video source into the VPS through the video input device;

executing the video source frames, by the processor;

dynamically and non-contiguously extracting key frames from the video source frames during the executing, said extracting implemented in accordance with a frame extraction algorithm that is stored in the memory structure and executed by the processor;

storing the extracted key frames in a first memory of the memory structure; and

terminating extracting key frames prior to completion of said executing of the video source frames.

Claim 2 (original): The method of Claim 1, wherein the first memory includes a temporary memory.

Claim 3 (original): The method of Claim 2, wherein the temporary memory includes a random access memory (RAM).

Claim 4 (original): The method of Claim 1, wherein the first memory includes a permanent memory.

Claim 5 (original): The method of Claim 4, wherein the permanent memory includes hard disk memory.

Claim 6 (original): The method of Claim 1, further comprising recording in the first memory an

indication of a video source frame being executed when the terminating occurred.

Claim 7 (original): The method of Claim 6, wherein recording in the first memory comprises generating a special key frame that includes the indication, and further comprising appending the special key frame to the extracted key frames in the first memory.

Claim 8 (original): The method of Claim 1, wherein the terminating is triggered by action of a user of the VPS.

Claim 9 (original): The method of Claim 8, wherein the action includes a manipulating by the user of a user input device.

Claim 10 (original): The method of Claim 1, wherein the terminating occurs at a time when a predetermined condition has occurred.

Claim 11 (original): The method of Claim 10, wherein the predetermined condition includes execution of a predetermined fraction or percentage of the video source frames.

Claim 12 (original): The method of Claim 10, wherein the predetermined condition includes execution of a predetermined number of video source frames.

Claim 13 (original): The method of Claim 10, wherein the predetermined condition includes an elapsing of predetermined time duration from initiation of executing the video source frames.

Claim 14 (original): The method of Claim 1, further comprising reviewing the key frames by a user of the VPS, wherein the reviewing occurs through an output display that is coupled to the processor.

Claim 15 (original): The method of Claim 14, wherein the output display includes a television screen or a computer monitor.

Claim 16 (original): The method of Claim 14, wherein the reviewing occurs prior to completion of executing the video source frames.

Claim 17 (original): The method of Claim 16, wherein the reviewing occurs at or after the terminating.

Claim 18 (original): The method of Claim 16, wherein the reviewing occurs prior to the terminating.

Claim 19 (original): The method of Claim 14, wherein the reviewing occurs at or after completion of executing the video source frames.

Claim 20 (original): The method of Claim 14, further comprising at or after completion of the reviewing, erasing the key frames from the first memory.

Claim 21 (original): The method of Claim 20, wherein the erasing is triggered by action of the user.

Claim 22 (original): The method of Claim 21, wherein the action includes a manipulating by the user of a user input device.

Claim 23 (original): The method of Claim 20, wherein the erasing occurs at a time when a predetermined condition has occurred.

Claim 24 (original): The method of Claim 23, wherein the predetermined condition includes completion of the executing of the video source frames.

Claim 25 (original): The method of Claim 23, wherein the predetermined condition includes an elapse of a predetermined amount of time following the reviewing.

Claim 26 (original): The method of Claim 14, futher comprising after completion of the

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reviewing, copying the key frames from the first memory to a second memory of the memory structure, wherein the second memory includes a removable memory.

Claim 27 (original): The method of Claim 26, further comprising after completion of the copying, erasing the key frames from the first memory.

Claim 28 (original): The method of Claim 1, wherein the video frame extraction algorithm comprises a content-based method of video frame extraction.

Claim 29 (original): The method of Claim 28, wherein the content-based method includes a keyframe scene detection method selected from the group consisting of a Method One keyframe scene detection method, a Method Two keyframe scene detection method, a Method Three keyframe scene detection method, and a Method Four keyframe scene detection method.

Claim 30 (original): The method of Claim 1, wherein the video frame extraction algorithm comprises a content-independent method of video frame extraction.

Claim 31 (previously presented): A display system for processing video frames, comprising:
    a video frame extraction algorithm within the display system that dynamically and non-contiguously extracts key frames from the video source frames during execution of the video source frames;
    a processor within the display system that executes the video source frames and executes the video frame extraction algorithm;
    a video input device within the display system that receives the video source frames from a video source, wherein the video input device is coupled to the processor;
    a memory structure within the display system that is coupled to the processor, wherein the memory structure stores the video frame extraction algorithm, and wherein a first memory of the memory structure stores the extracted key frames; and
    a terminating mechanism within the display system that terminates extraction of the key frames prior to completion of execution of the video source frames.

Claim 32 (original): The system of Claim 31, wherein the first memory includes a temporary memory.

Claim 33 (original): The system of Claim 32, wherein the temporary memory includes a random access memory (RAM).

Claim 34 (original): The system of Claim 31, wherein the first memory includes a permanent memory.

Claim 35 (original): The system of Claim 34, wherein the permanent memory includes hard disk memory.

Claim 36 (original): The system of Claim 31, further comprising a recording mechanism that records in the first memory an indication of a video source frame being executed when the extraction of key frames is terminated.

Claim 37 (original): The system of Claim 36, wherein the receiving mechanism records the indication in a special key frame that is appended to the extracted key frames.

Claim 38 (original): The system of Claim 31, wherein the terminating mechanism includes a user-controlled device.

Claim 39 (original): The system of Claim 38, wherein the user-controlled device includes a user input device that is coupled to the processor.

Claim 40 (original): The system of Claim 31, wherein the terminating mechanism terminates the extracting at a time when a predetermined condition has occurred.

Claim 41 (original): The system of Claim 40, wherein the predetermined condition includes execution of a predetermined fraction or percentage of the video source frames.

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Claim 42 (original): The system of Claim 40, wherein the predetermined condition includes execution of a predetermined number of video source frames.

Claim 43 (original): The system of Claim 40, wherein the predetermined condition includes an elapsing of predetermined time duration from initiation of the execution of the video source frames.

Claim 44 (original): The system of Claim 31, further comprising an output display through which a user may review the extracted key frames, wherein the output display is coupled to the processor.

Claim 45 (original): The system of Claim 44, wherein the output display includes a television screen or a computer monitor.

Claim 46 (original): The system of Claim 44, wherein the system permits review of the key frames prior to completion of execution of the video source frames.

Claim 47 (original): The system of Claim 46, wherein the system permits review of the key frames when or after the terminating mechanism terminates extracting the key frames.

Claim 48 (original): The system of Claim 46, wherein the system permits review of the key frames before the terminating mechanism terminates extracting the key frames.

Claim 49 (original): The system of Claim 44, wherein the system permits receive of the key frames upon or after completion of execution of the video source frames.

Claim 50 (currently amended): The system of Claim 44, further comprising ~~and~~ an erasing mechanism that erases the key frames from the first memory at or after completion of review of the key frames by the user.

Claim 51 (original): The system of Claim 50, wherein the erasing mechanism is triggered by

action of the user.

Claim 52 (original): The system of Claim 51, further comprising a user input device, wherein the action includes the user manipulation of the user input device.

Claim 53 (original): The system of Claim 50, wherein the erasing mechanism is triggered when a predetermined condition has occurred.

Claim 54 (original): The system of Claim 53, wherein the predetermined condition includes completion of execution of the video source frames.

Claim 55 (original): The system of Claim 53, wherein the predetermined condition includes an elapse of a predetermined amount of time following the review of the key frames.

Claim 56 (original): The system of Claim 31, further comprising a second memory of the memory structure and a transferring mechanism, wherein the transferring mechanism transfers the key frames from the first memory to the second memory, and wherein the second memory includes a removable memory.

Claim 57 (original): The system of Claim 56, further comprising an erasing mechanism that erases the key frames from the first memory after the transferring mechanism completes transfer of the keyframes from the first memory to the second memory.

Claim 58 (original): The system of Claim 31, wherein the video frame extraction algorithm comprises a content-based method of video frame extraction.

Claim 59 (original): The system of Claim 58, wherein the content-based method includes a keyframe scene detection method selected from the group consisting of a Method One keyframe scene detection method, a Method Two keyframe scene detection method, a Method Three keyframe scene detection method, and a Method Four keyframe scene detection method.

Claim 60 (original): The system of Claim 31, wherein the video frame extraction algorithm comprises a content-independent method of video frame extraction.